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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s):

**FUNABASHI** 

Serial No.:

09/902,672

Filed:

July 12, 2001

For:

METHOD OF MANUFACTURING A

SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE

Group:

2825

Examiner:

L. Malsawma

## SUPPLEMENTAL AMENDMENT

Assistant Commissioner for Patents Washington, D.C. 20231

May 10, 2002

Sir:

Supplementing the Amendment filed May 6, 2002, please amend the above-identified application as follows:

## **IN THE CLAIMS**

Please amend the claims presently in the application as follows:

MAY 13 2002

25. (Amended) A method of manufacturing a semiconductor integrated circuit device according to claim 17, wherein said cleaning is performed with the processing solution at a temperature as low as 40°C.

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